

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

② <i>ph</i>	O.I.P.E.	PATENT DATE
SCANNED <i>AA</i>	Q.A. <i>21</i>	

CI	APPLICATION NO. 09/604519	CONT/PRIOR	CLASS 324 257	SUBCLASS 779	ART UNIT 2827 2836 2814	EXAMINER G. K. B. L.
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APPLICANTS

Vivian Ryan
Thomas Shilling

TITLE

integrated circuit and a method of manufacturing an integrated circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ 	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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